PATENT NUMBER ISSUE CLASSIFICATION U.S. UTILITY Patent Application EXAMINER APPLICATION NO. CONT/PRIOR CLASS SUBCLASS ART UNIT 2811 09/730875 DF 257 Loke 347 Saboshi Ishida Yesuo Nakahaga Ishida Hiroyuki Kuriyama Tsutomu Yamada Thin film transistor and method of Fabricating the same ISSUING CLASSIFICATION CROSS REFERENCE(8) M SUBCLASS (ONE SUBCLASS PER BLOCK) BEST AVAILABLE COPY INTERNATIONAL CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<u> </u>				NOTICE OF ALLOWANCE MAILED	
The term of this patent subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)				
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